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**Patent Application**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Geun-Hee Cho                      Docket: 8021-180 (SS-18400-US)  
Serial No: 10/716,146                      Group Art Unit: 2816  
Filing Date: November 18, 2003                      Examiner: Nguyen, Minh T.  
For: **TIME DELAY COMPENSATION CIRCUIT COMPRISING  
DELAY CELLS HAVING VARIOUS UNIT TIME DELAYS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO RESTRICTION REQUIREMENT**

Examiner:

In response to the Office Action dated March 15, 2005, applicant provisionally elects the claims directed towards the Group I (claim nos. 1-6 and 14-17), with traverse. Further, applicant provisionally elects Species I (claim 15), Sub-Species Ia (claim 4). Claims 1 to 17 are pending.

The Examiner has indicated that claims 1-3 and 14 are generic to a plurality of disclosed patentably distinct species in Group I. Claim 15 is believed to be generic to at least claims 4-6, and thus may be characterized under Species I in Group I, but not any of Sub-Species Ia-Ic as outlined in the Office Action. Therefore, while applicant has provisionally elected Sub-Species Ia

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Dated: April 15, 2005

  
Melithza Rodriguez

(claim 4), claim 15 should also be examined. Accordingly, examination of at least claims 1-4, 14 and 15 is requested.


As set forth in MPEP §803, if the search and examination of an entire application can be made without serious burden, the Examiner must examine it on the merits, even though the application contains claims to independent and distinct inventions.

The Examiner stated that the Group I claims, 1-6 and 14-17 are classified in class 327, subclass 158, and the Group II claims, 7-13, are classified in class 327, subclass 276. While the delay control circuit of the Group I and the synchronous mirror delay of Group II may be distinct for the reasons set forth by the Examiner, applicant believes that simultaneous examination will not present an undue burden. For example, both subclass 158, (with variable delay means) and subclass 276 (single output with variable or selectable delay) can be found within subclass 100 (signal converting, shaping, or generating). Under such circumstances, the Examiner is encouraged to maintain all claims in the same application.

Accordingly, withdrawal of the restriction requirement and examination of all claims is respectfully requested.

Respectfully submitted,

By:

  
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